

1/28

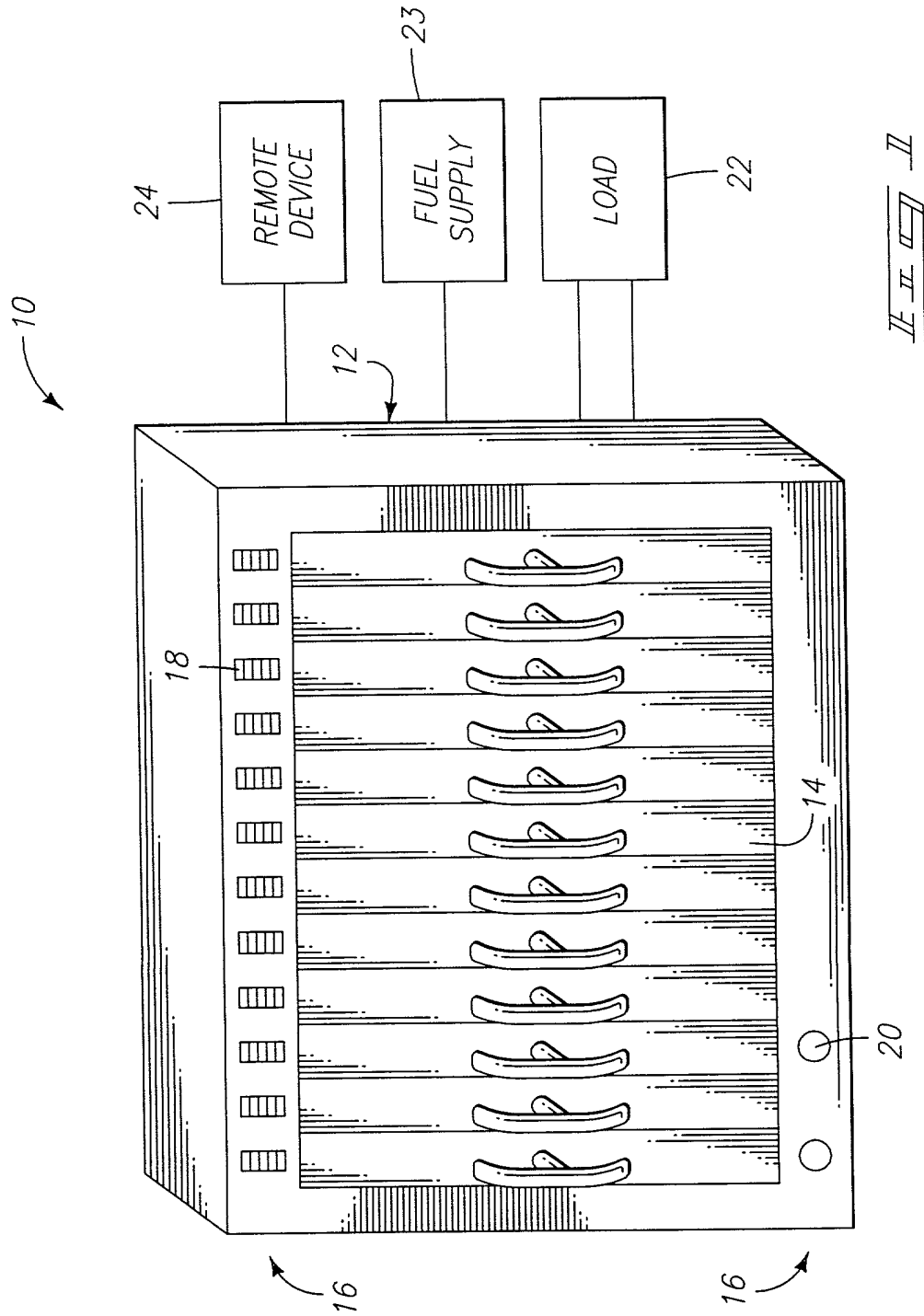
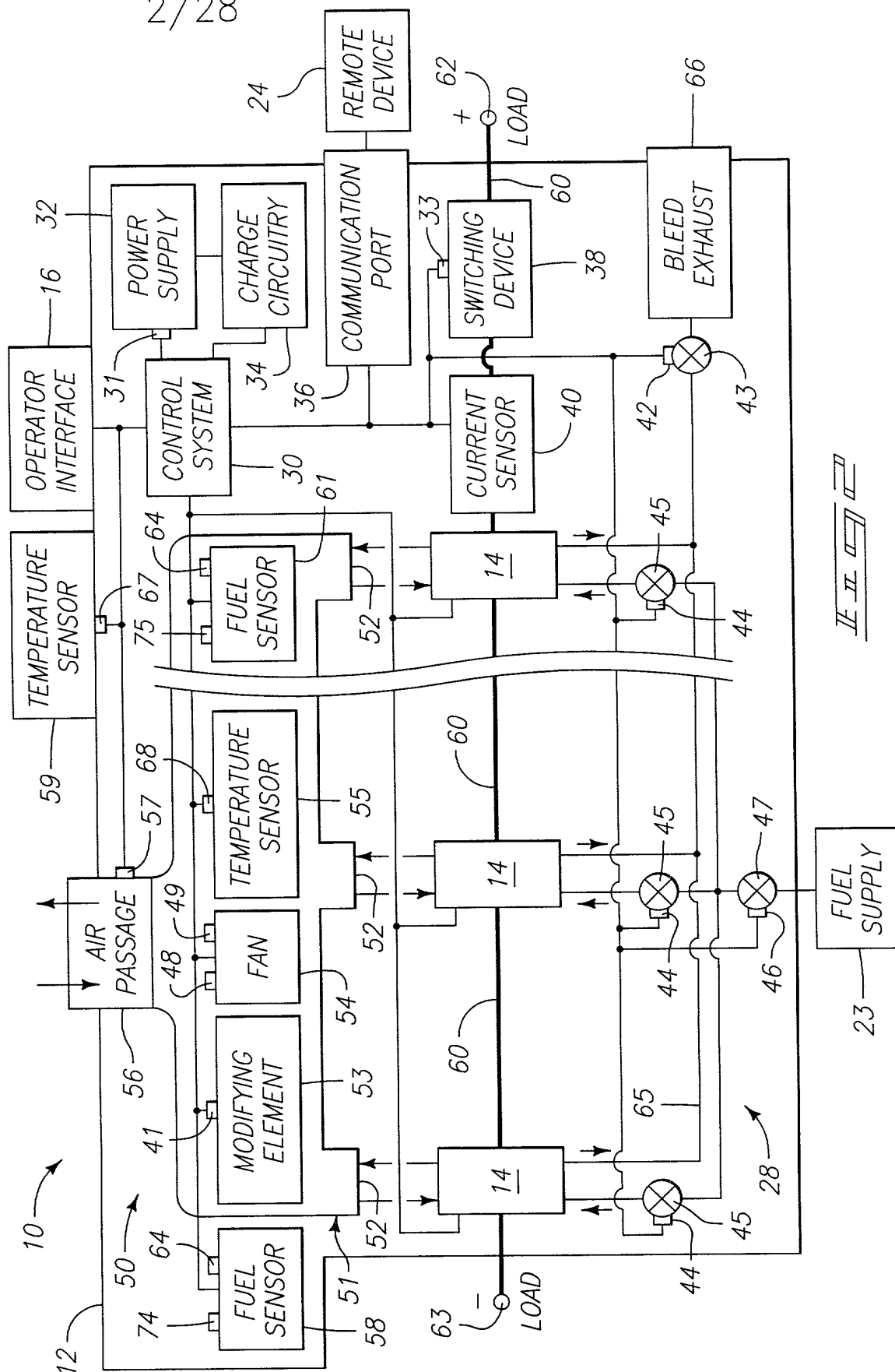
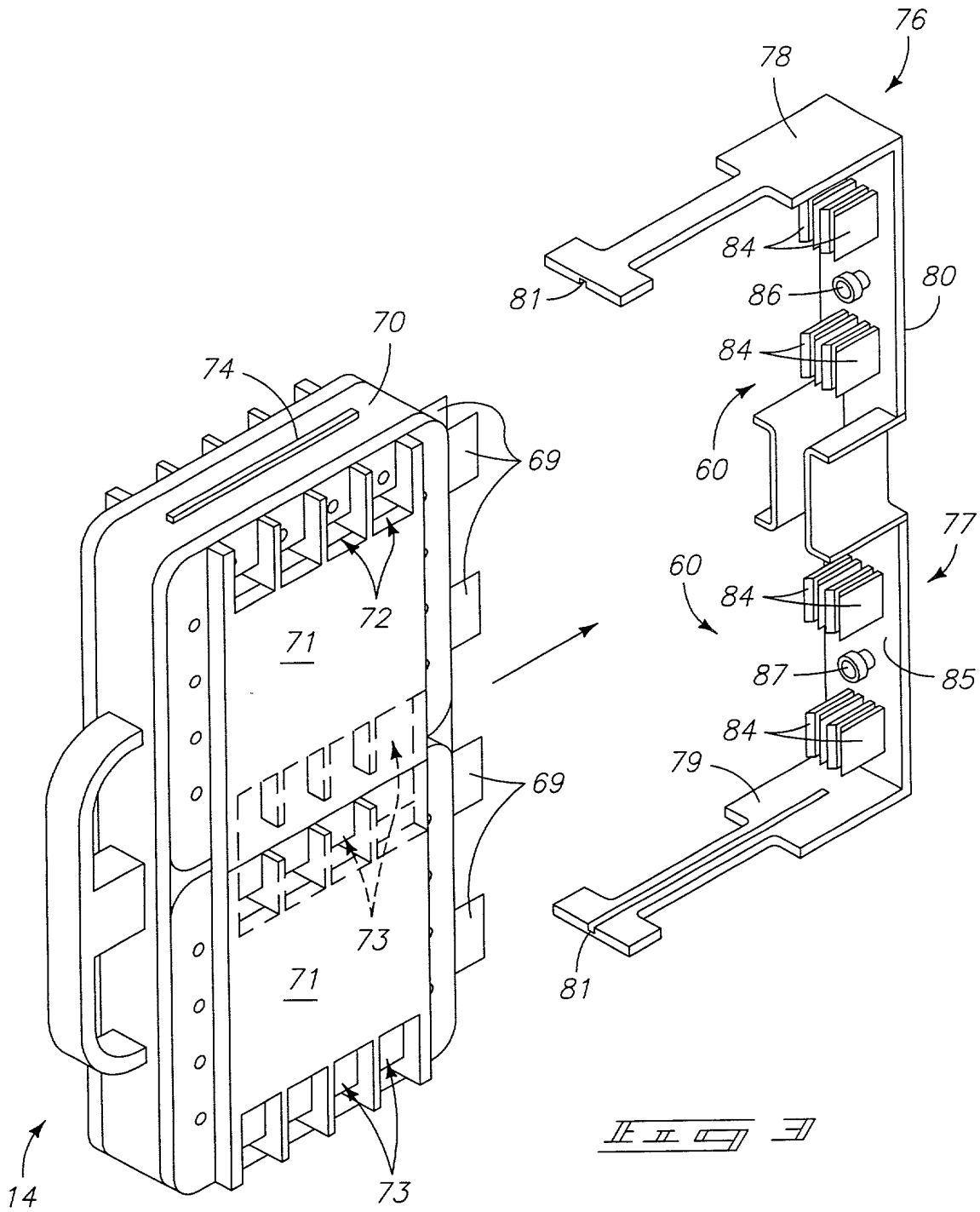


FIG. 1



3/28



4/28

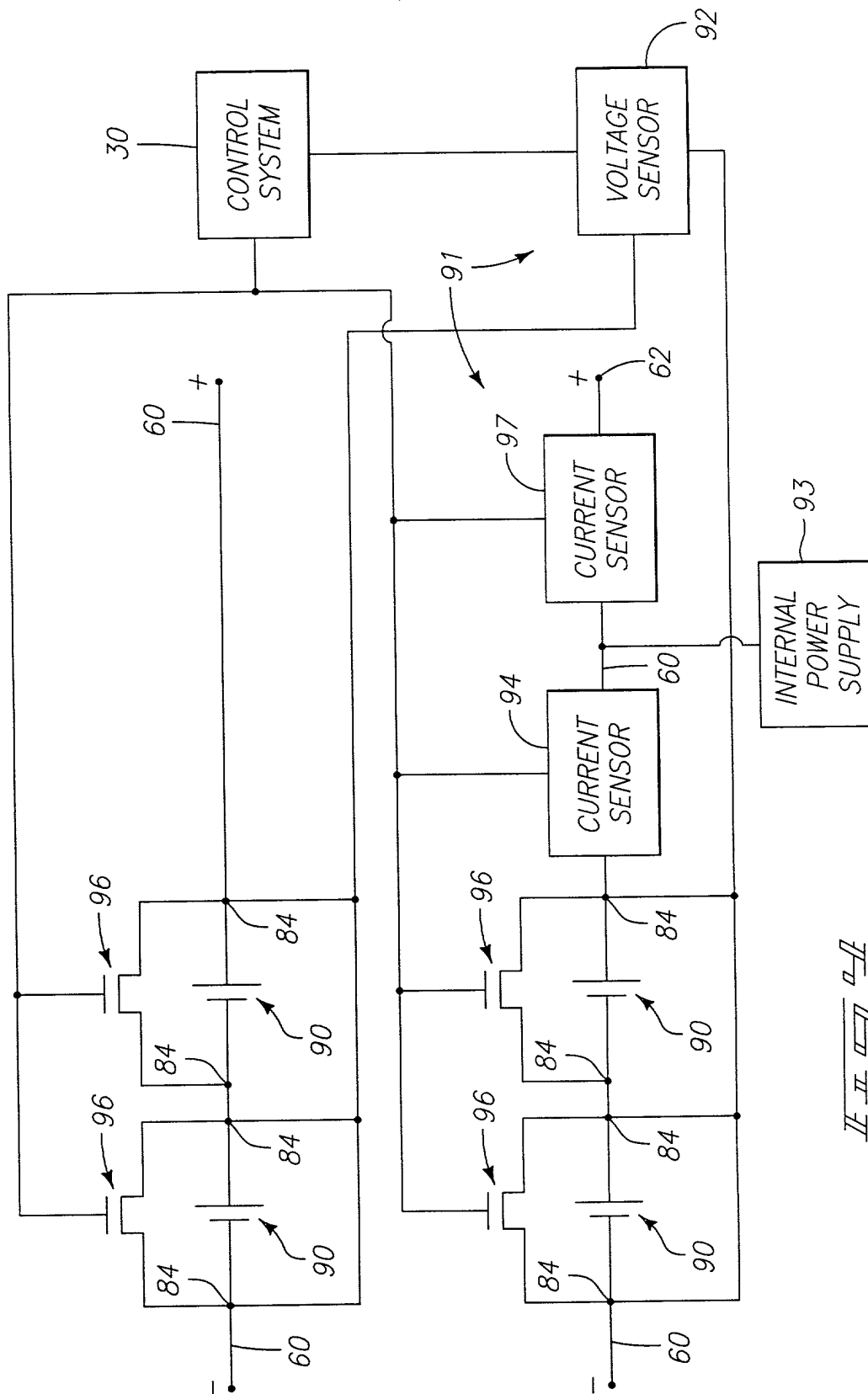


FIG. 2

FIG. 2 of 310660

5/28

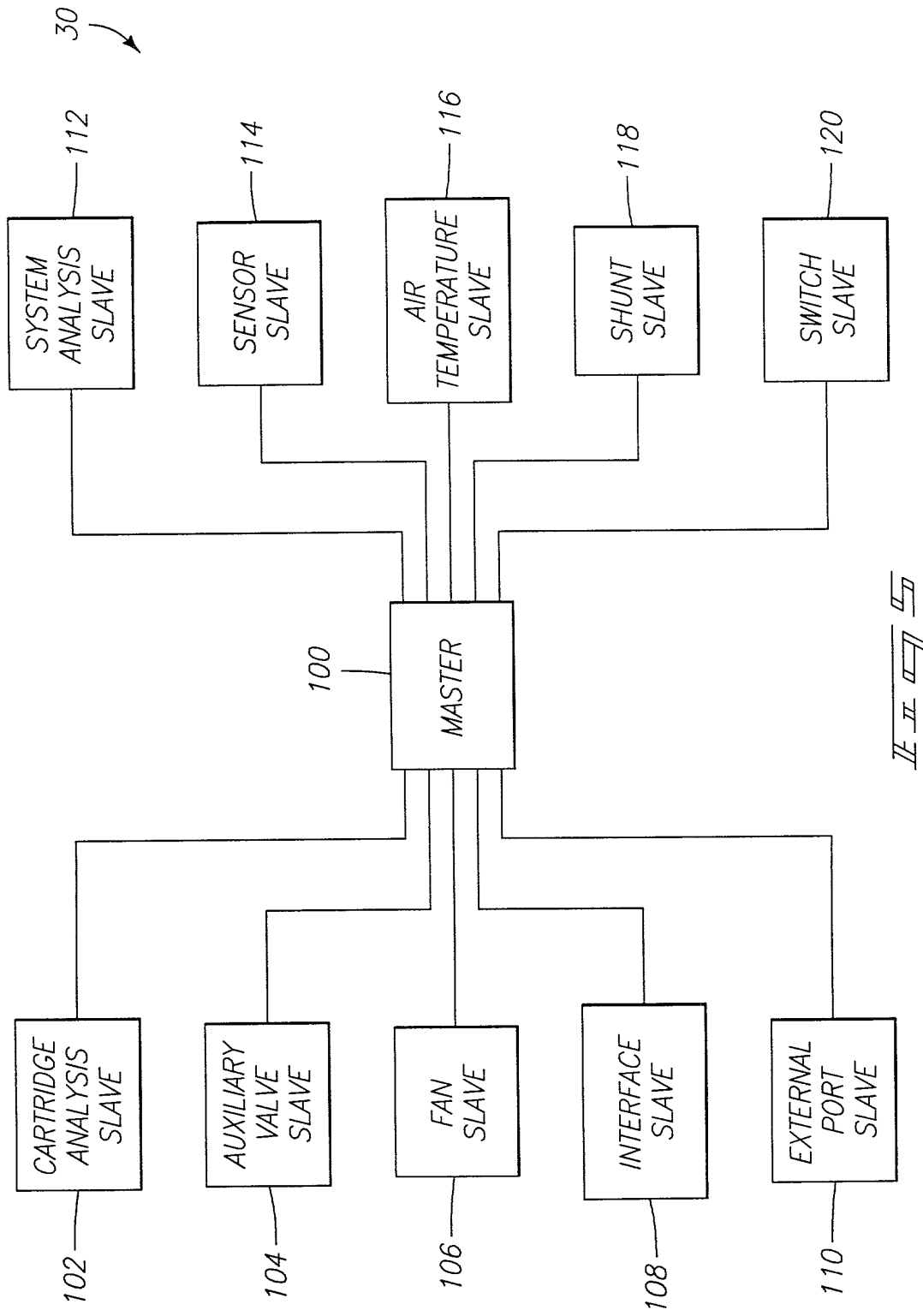
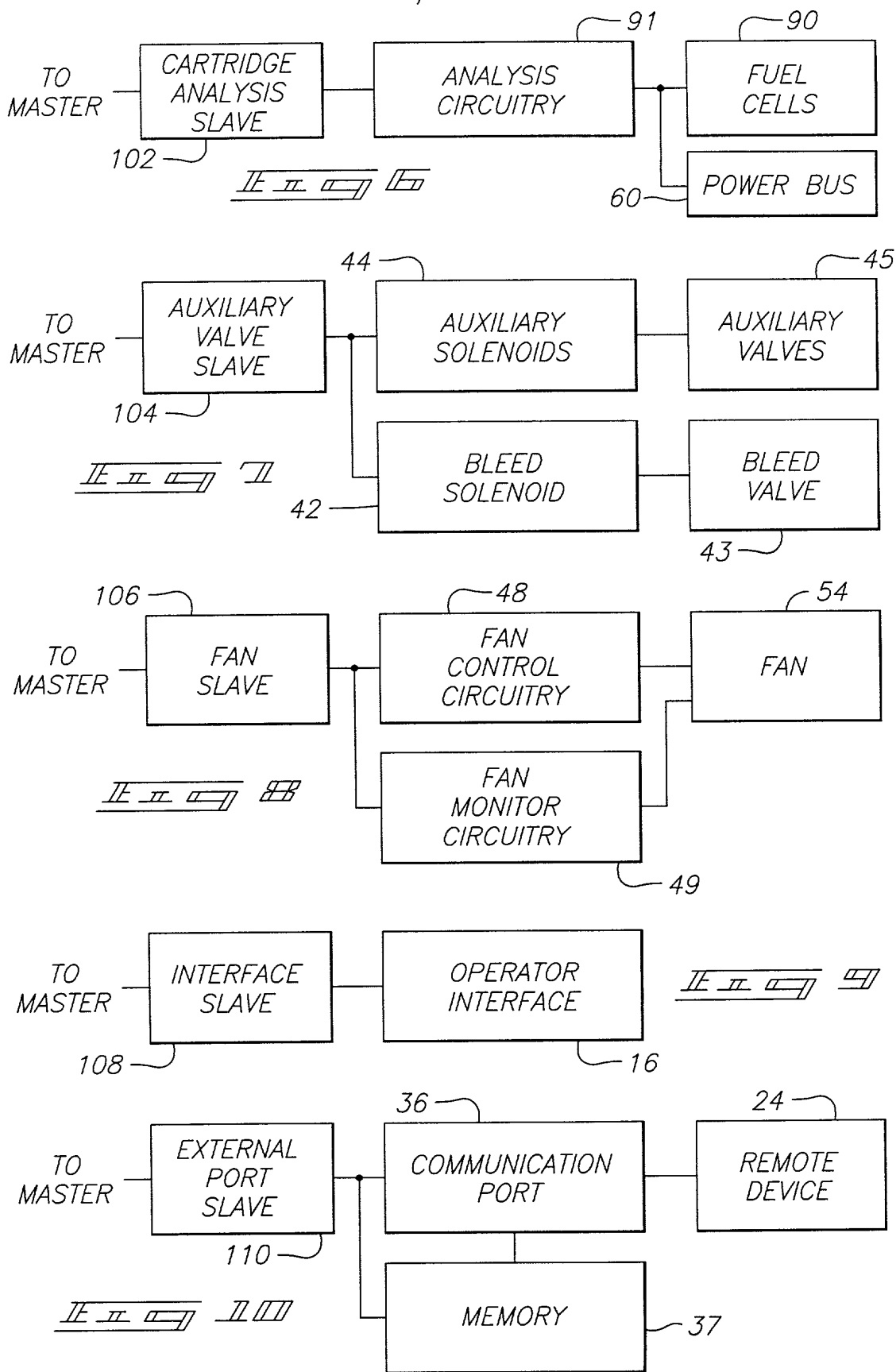


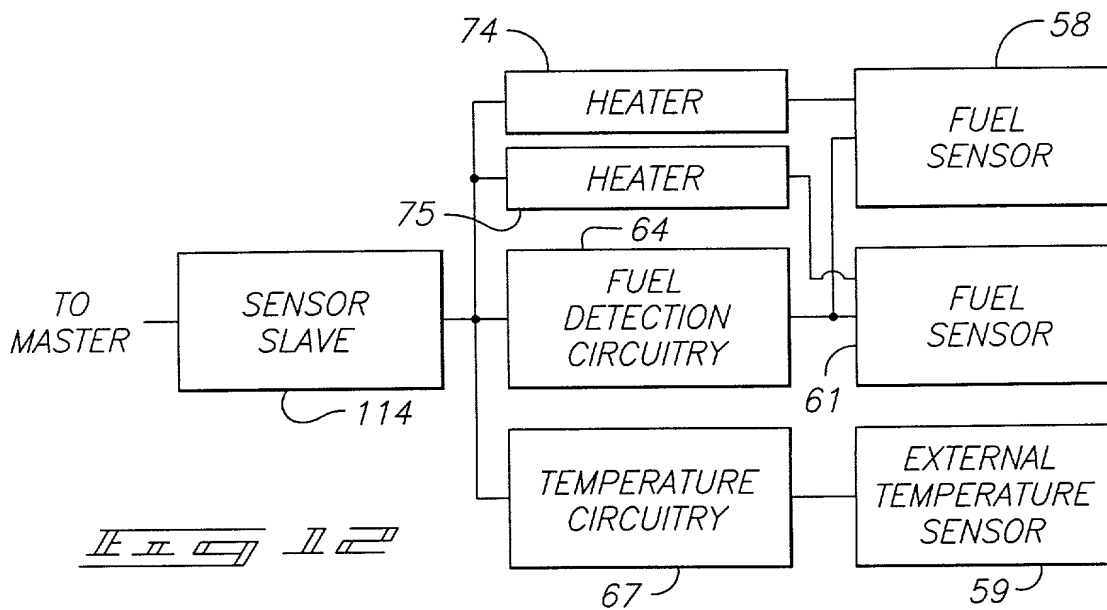
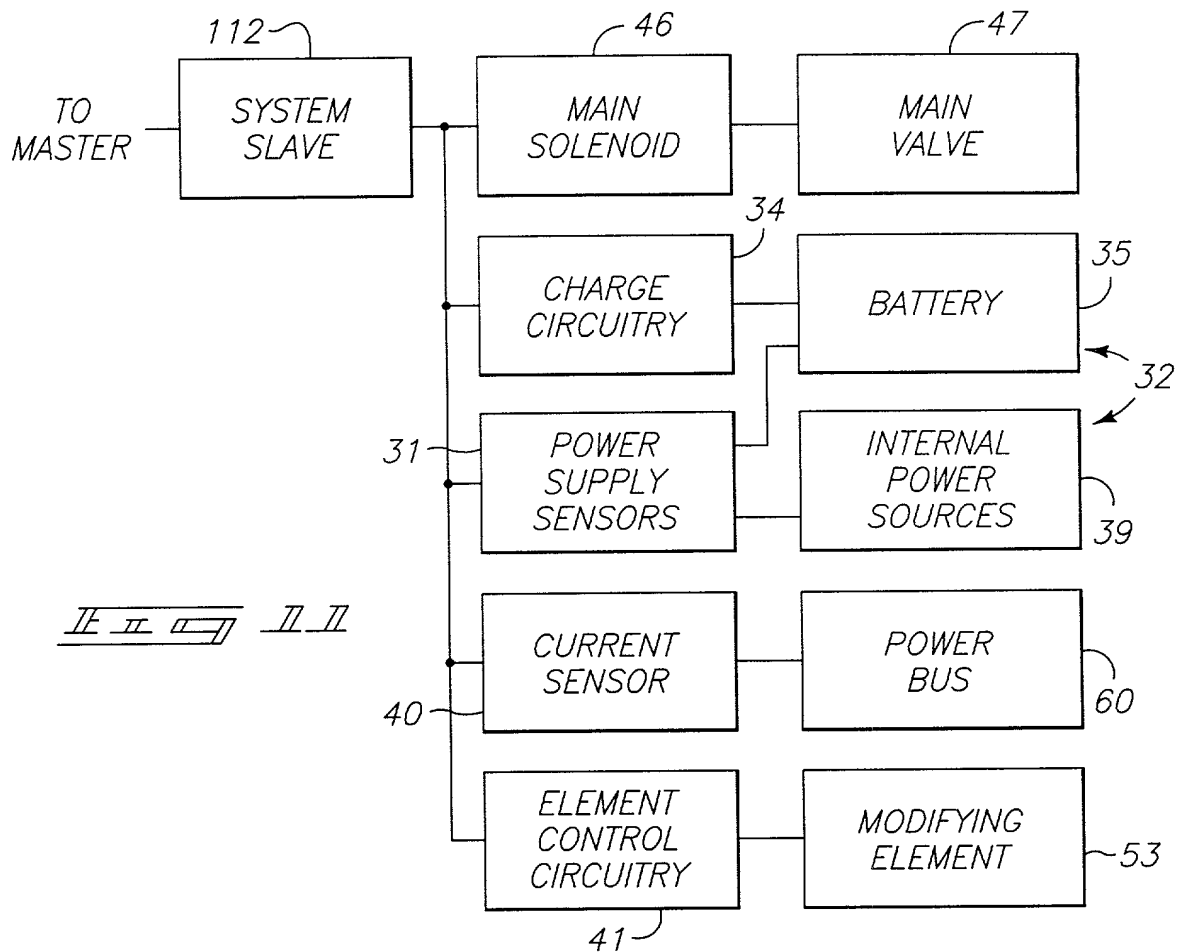
FIG. 5

FIG. 5

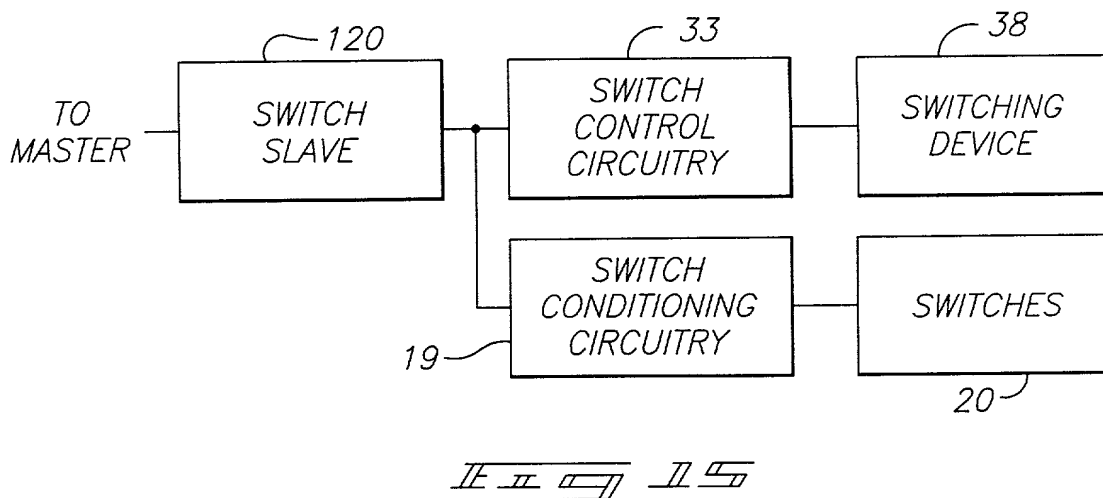
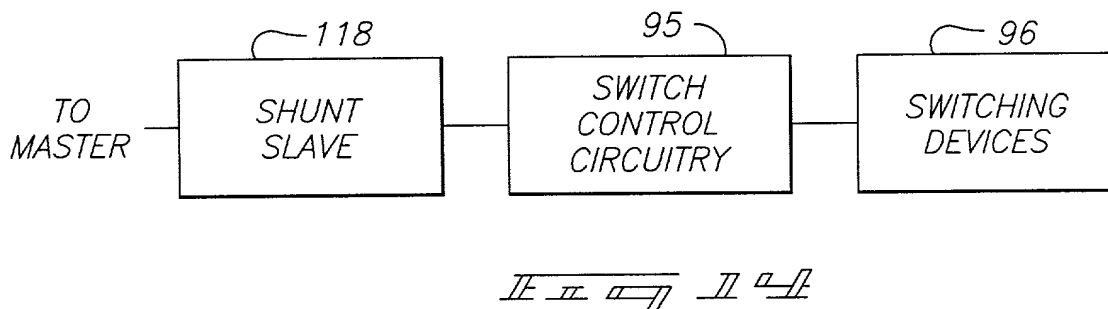
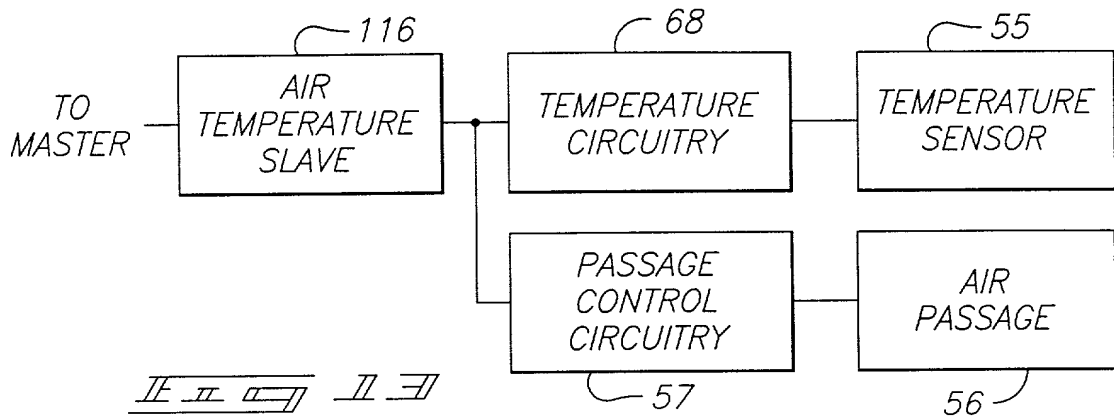
6/28



7/28

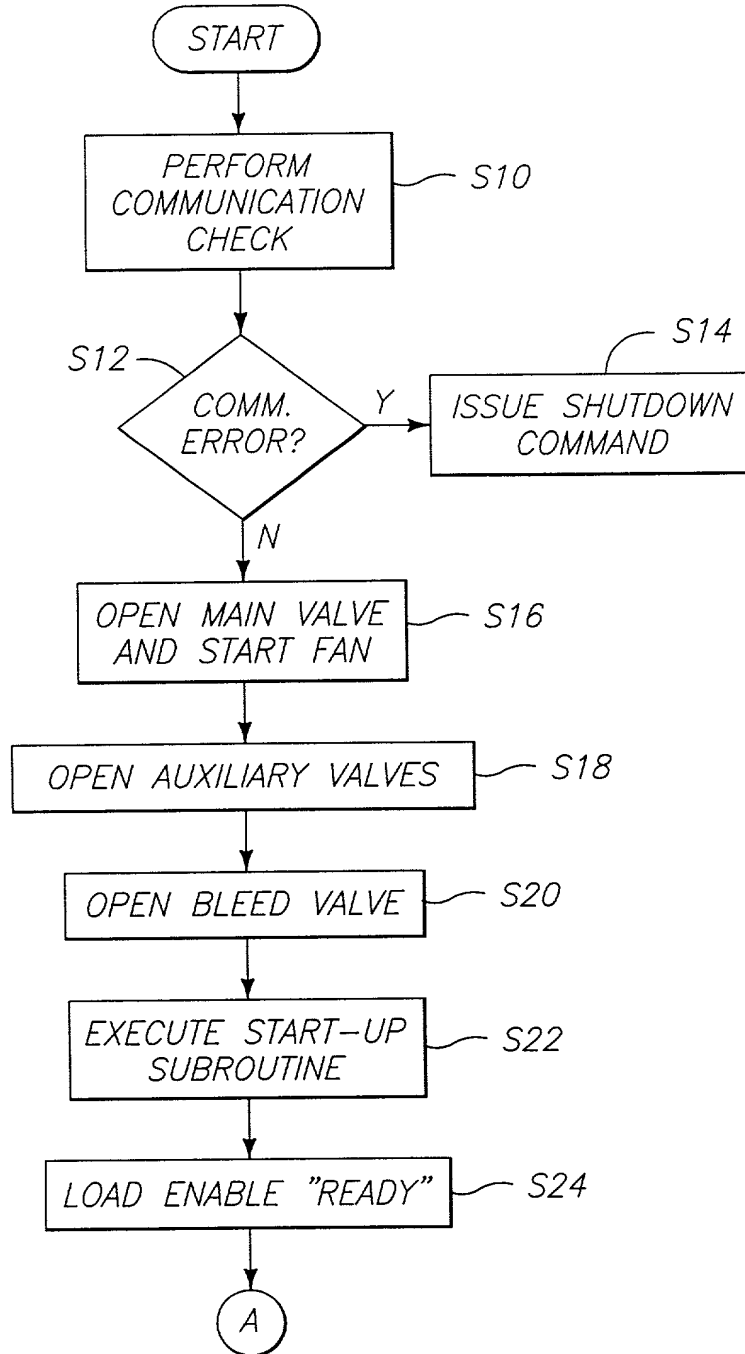


8/28

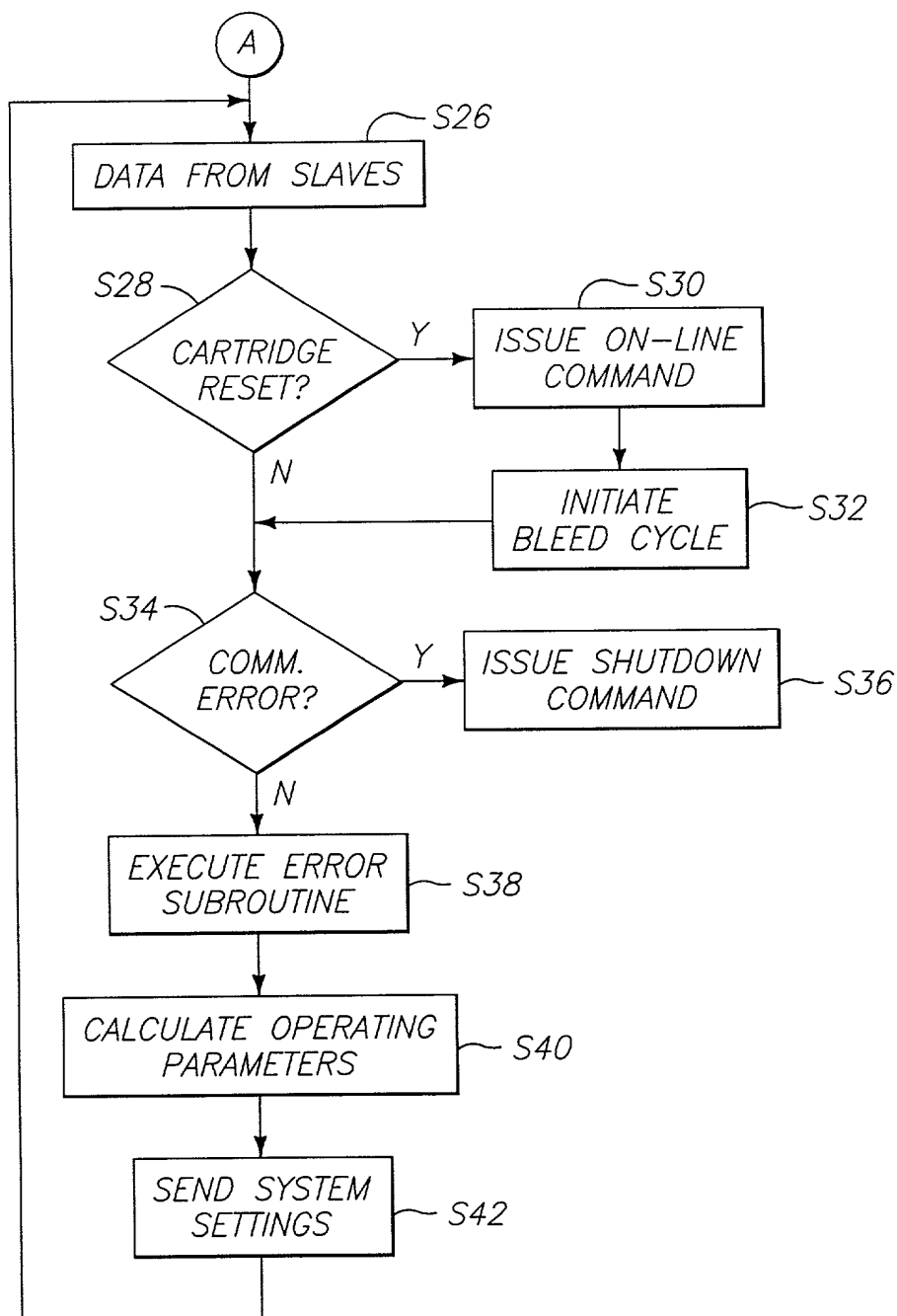




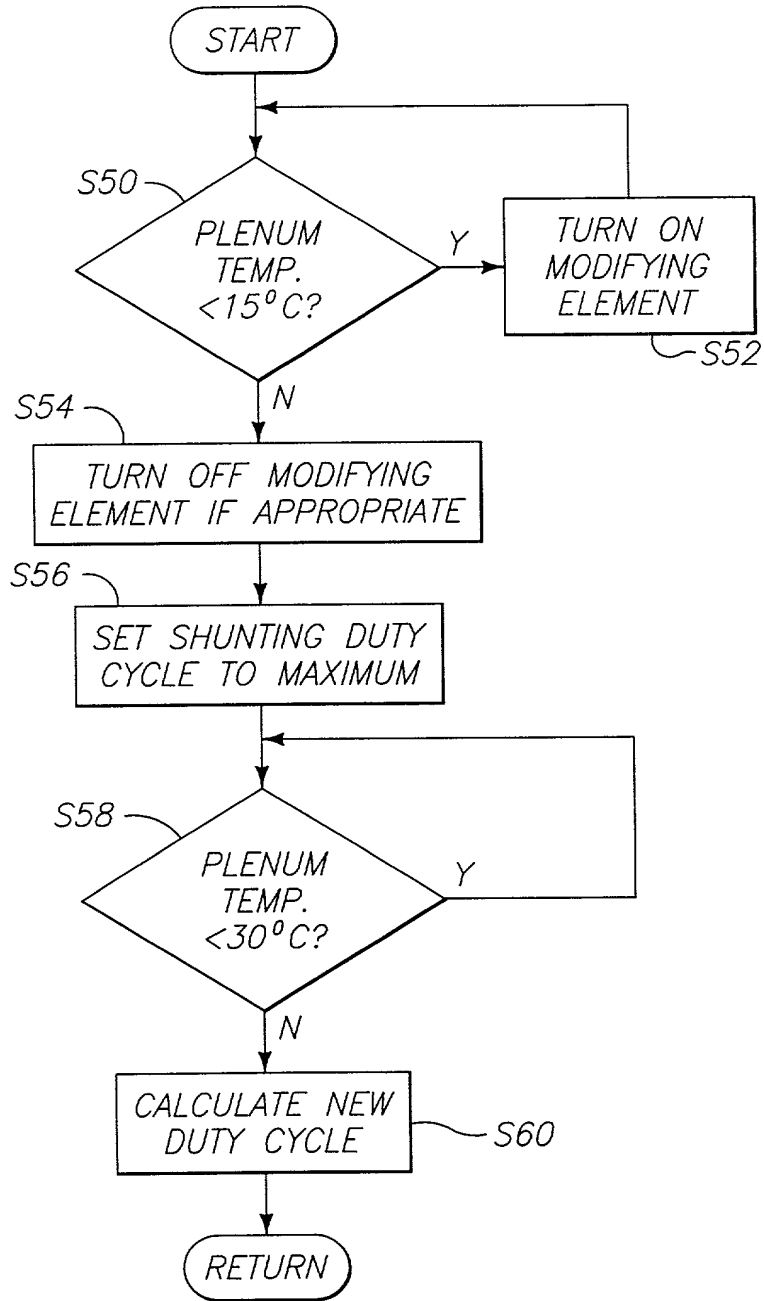
9/28

11 11 11 11

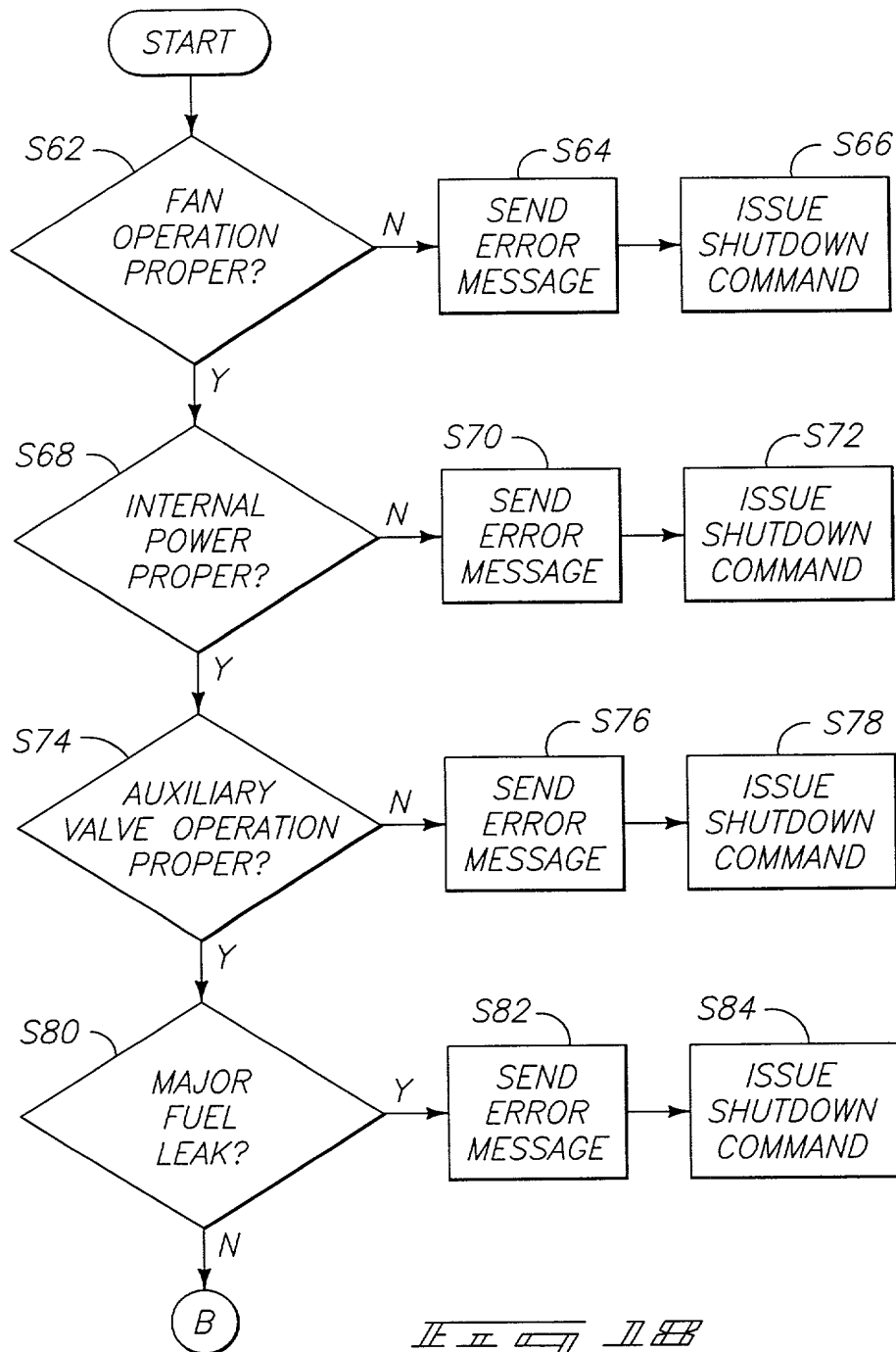
II II 7 16 A



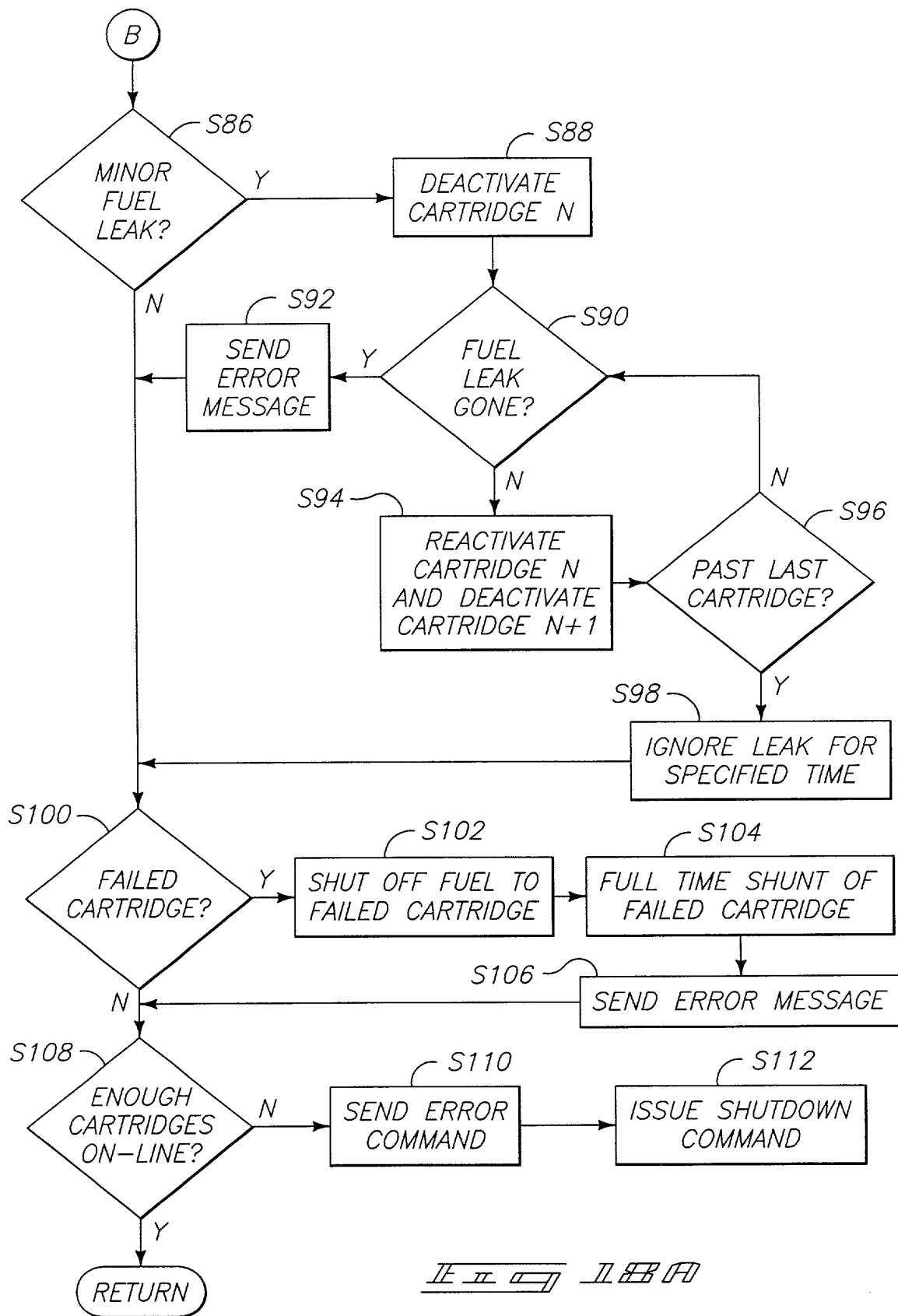
11/28

IF II II II

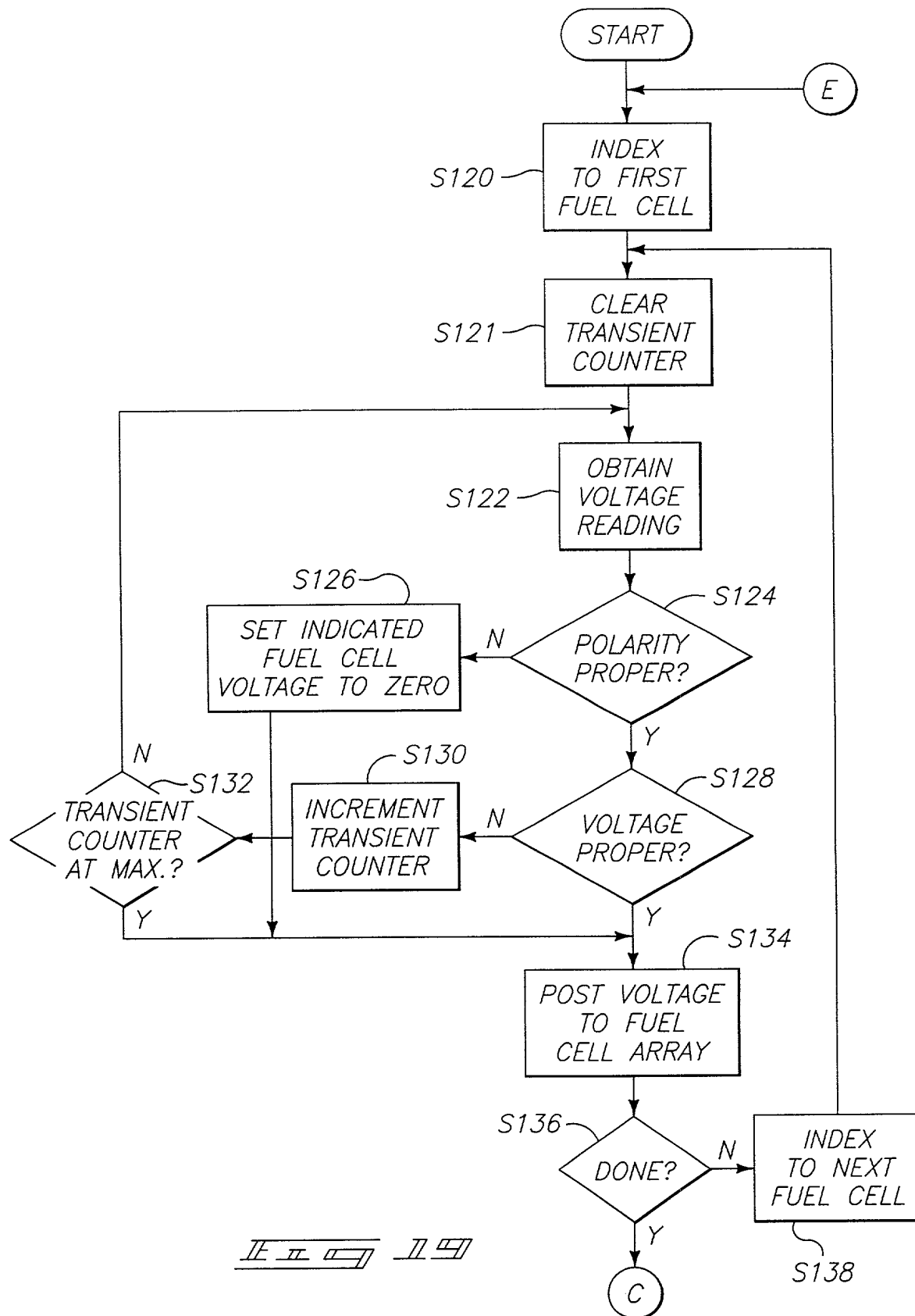
12/28

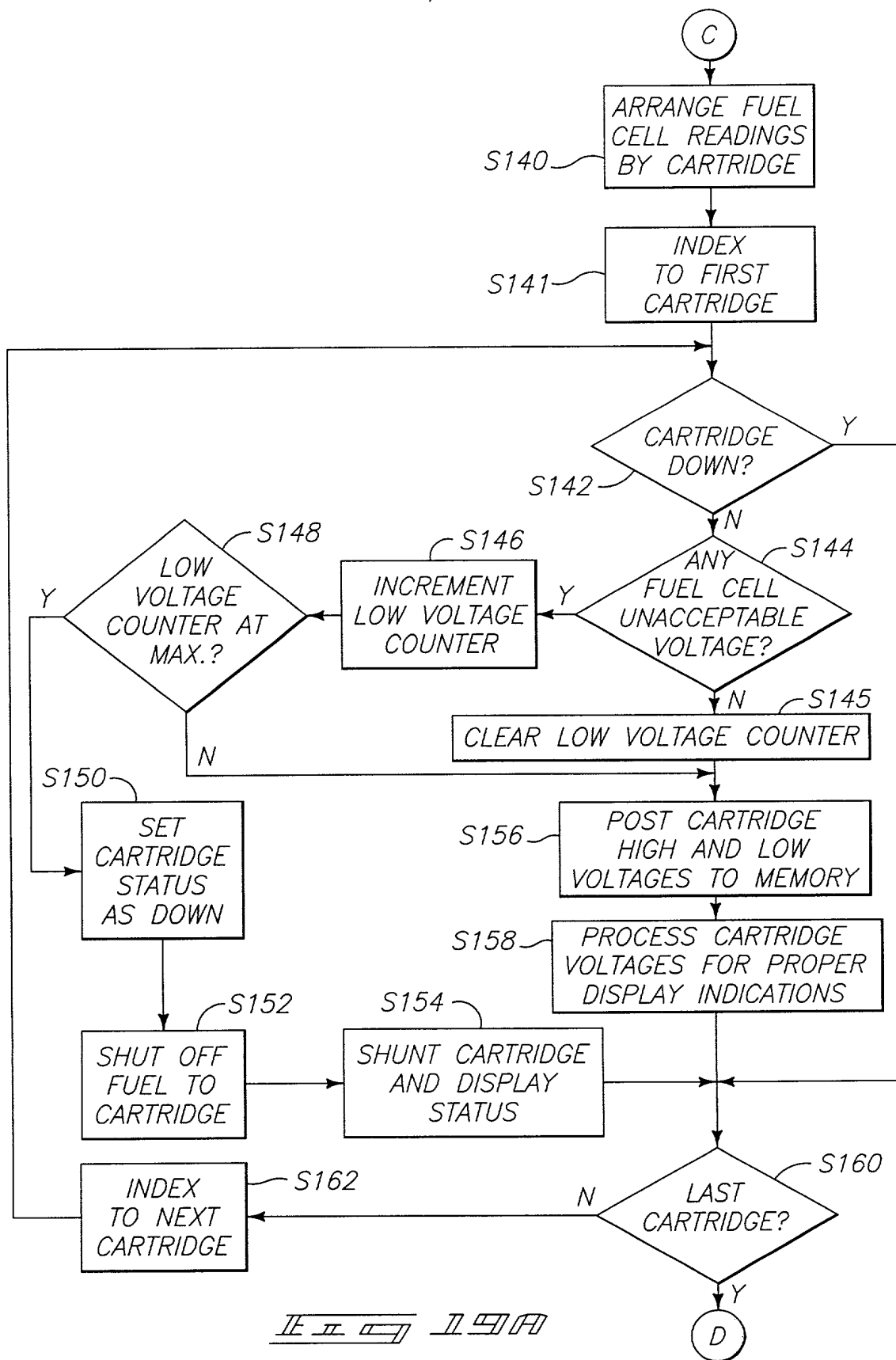


13/28

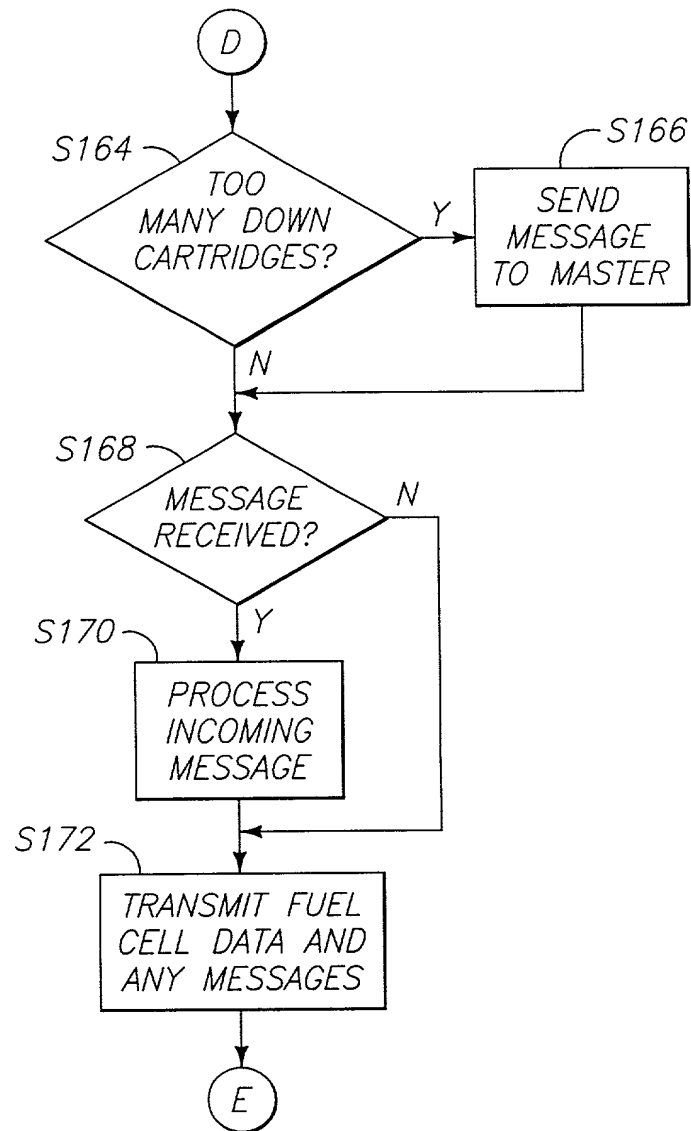
IEEE 1181A

14/28



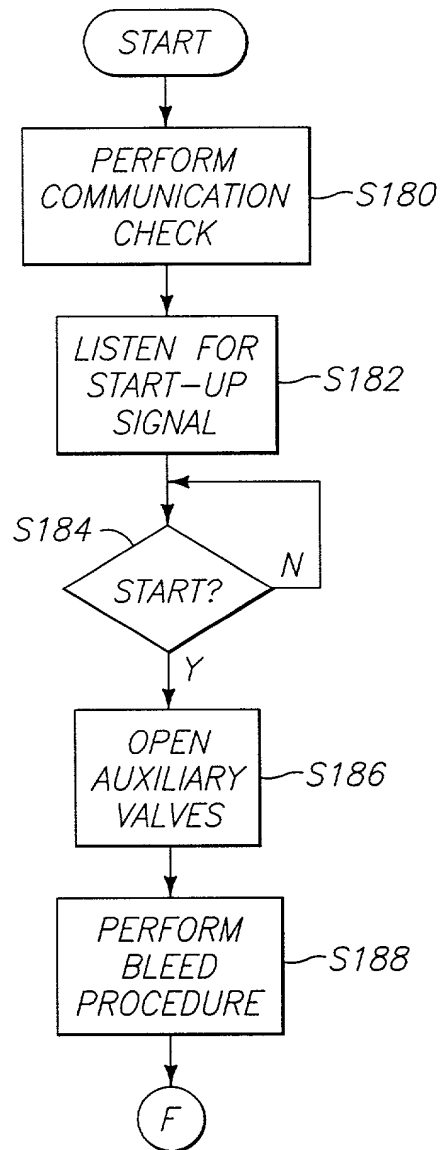


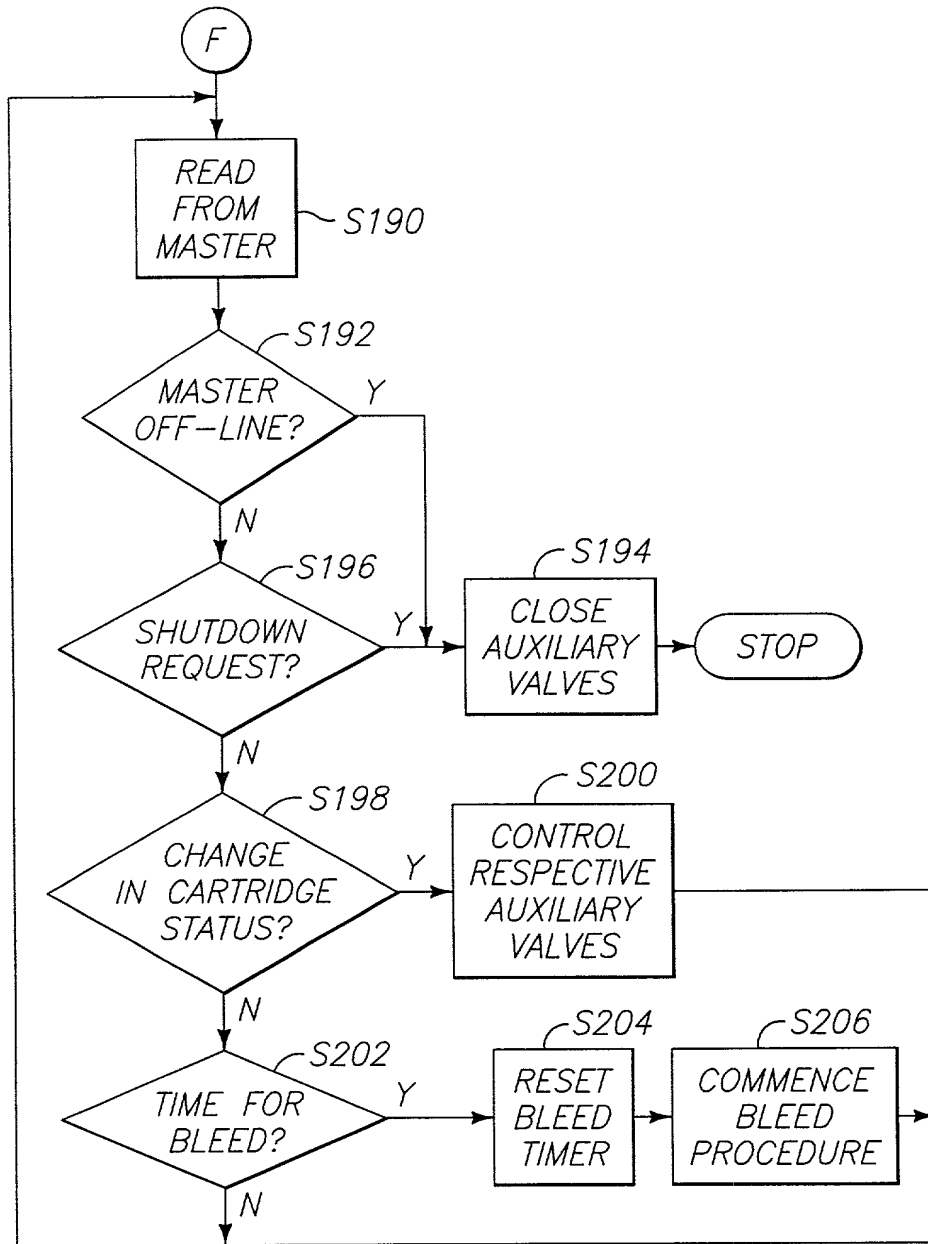
16/28



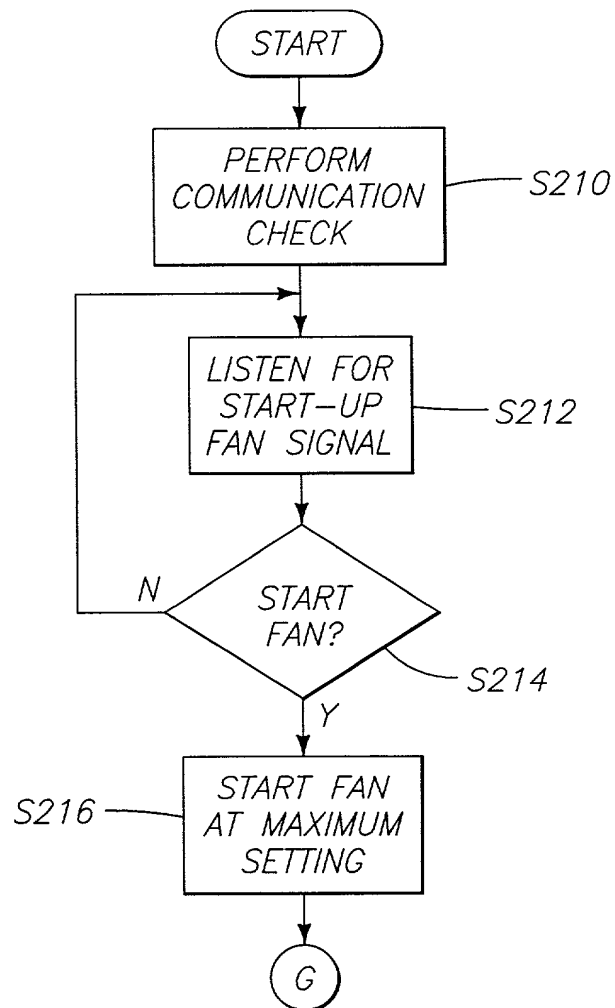


17/28

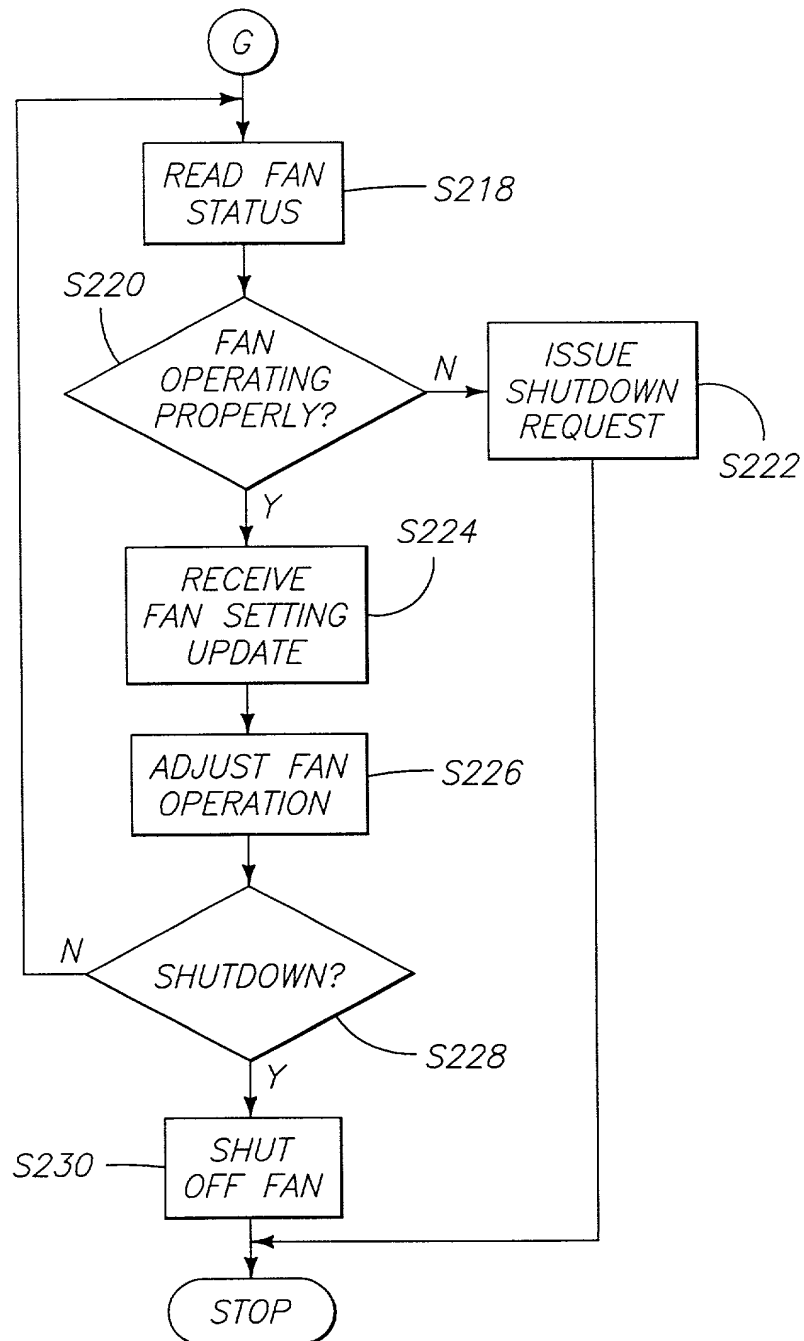




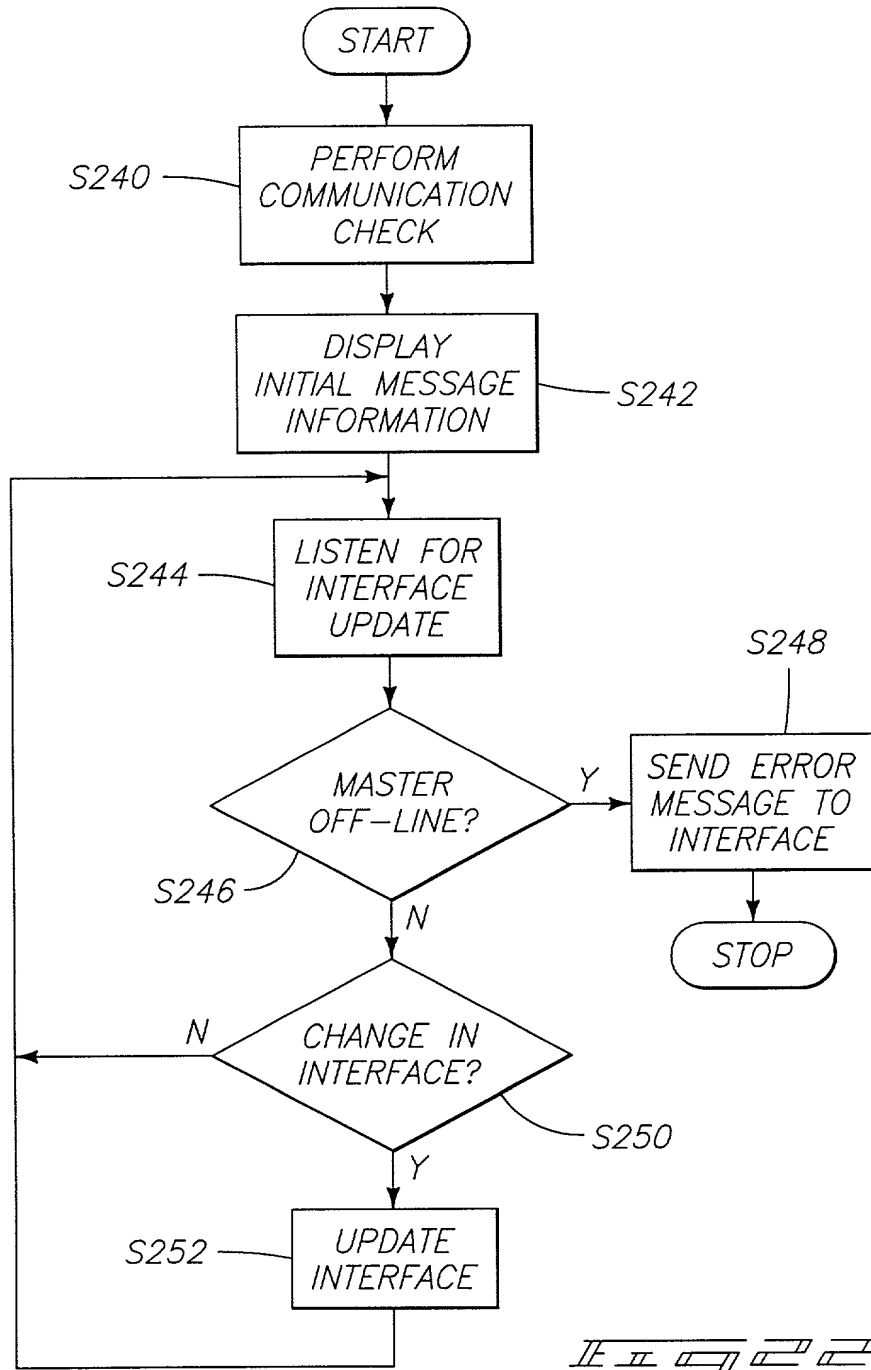
19/28



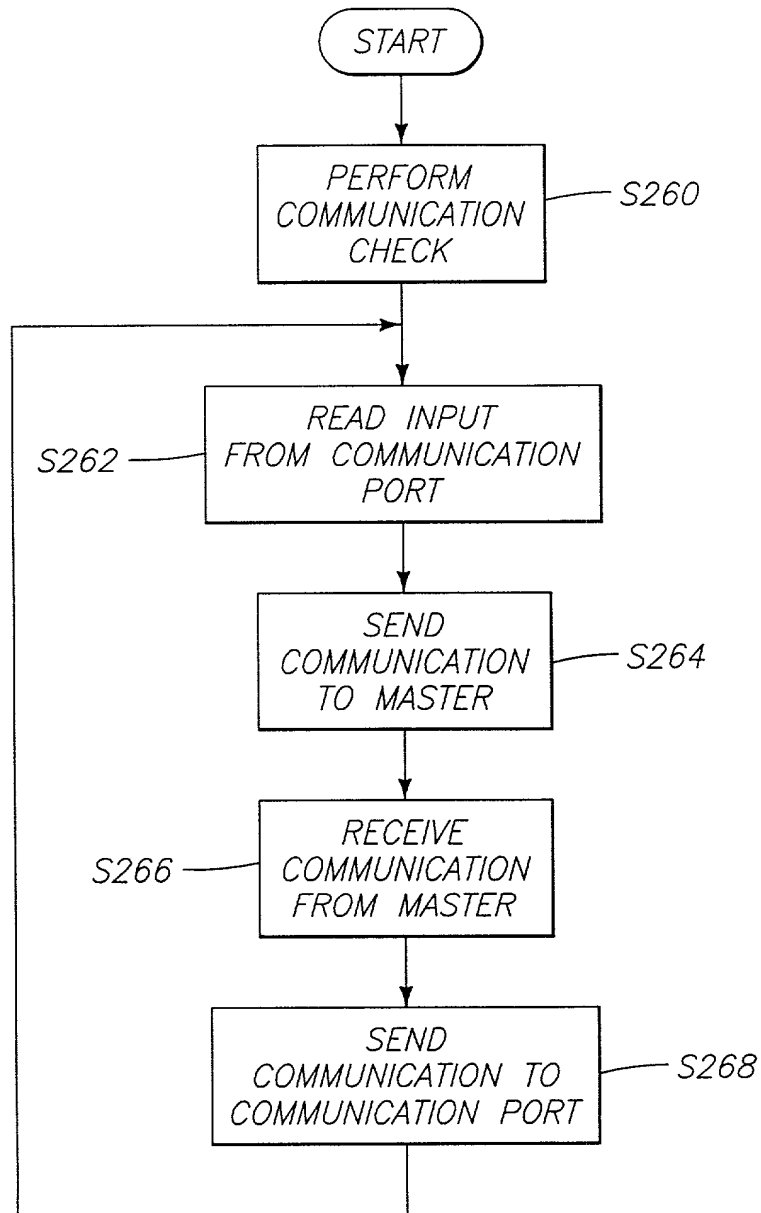
20/28

FIG. 21A

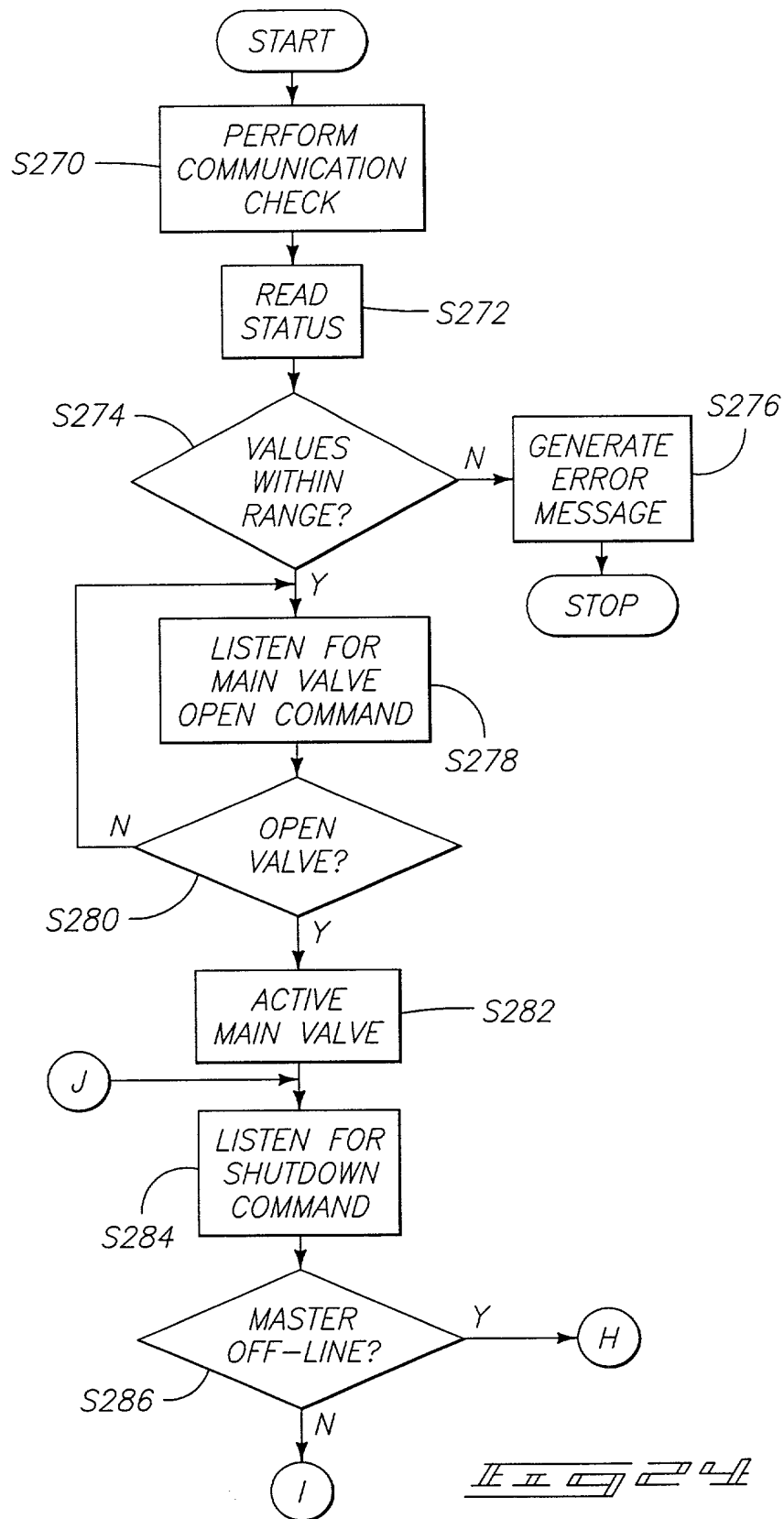
21/28



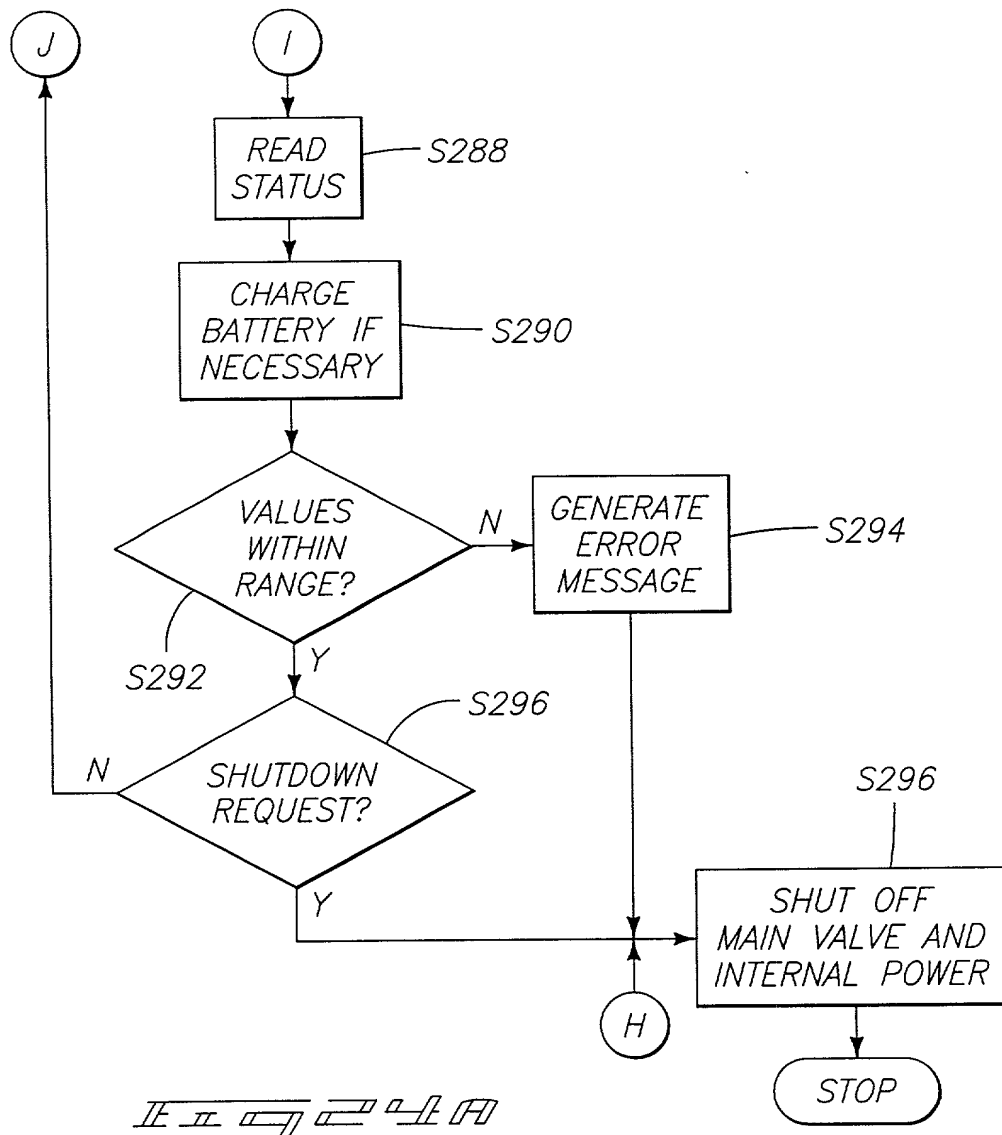
22/28

IEEE 802.3

23/28

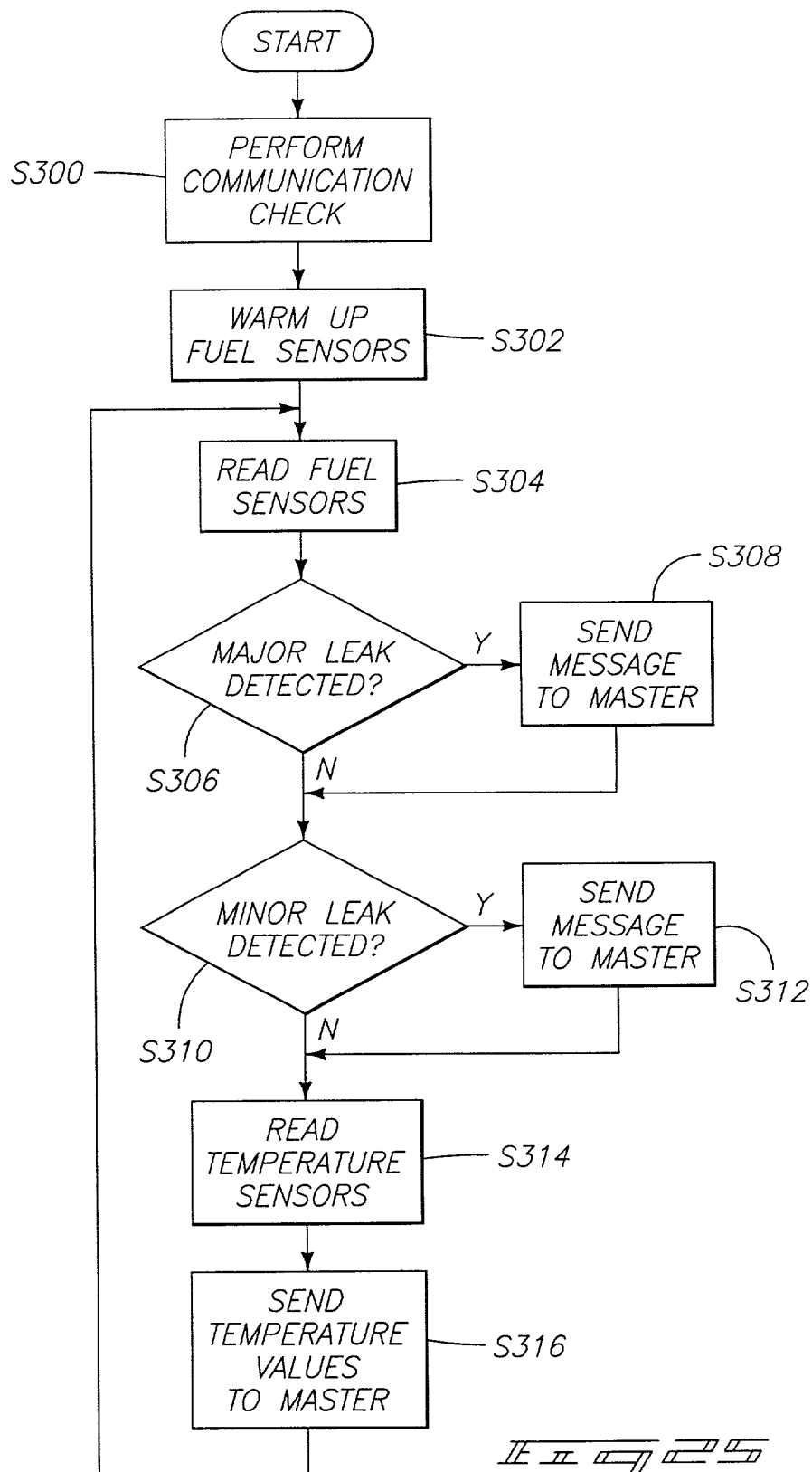


24/28

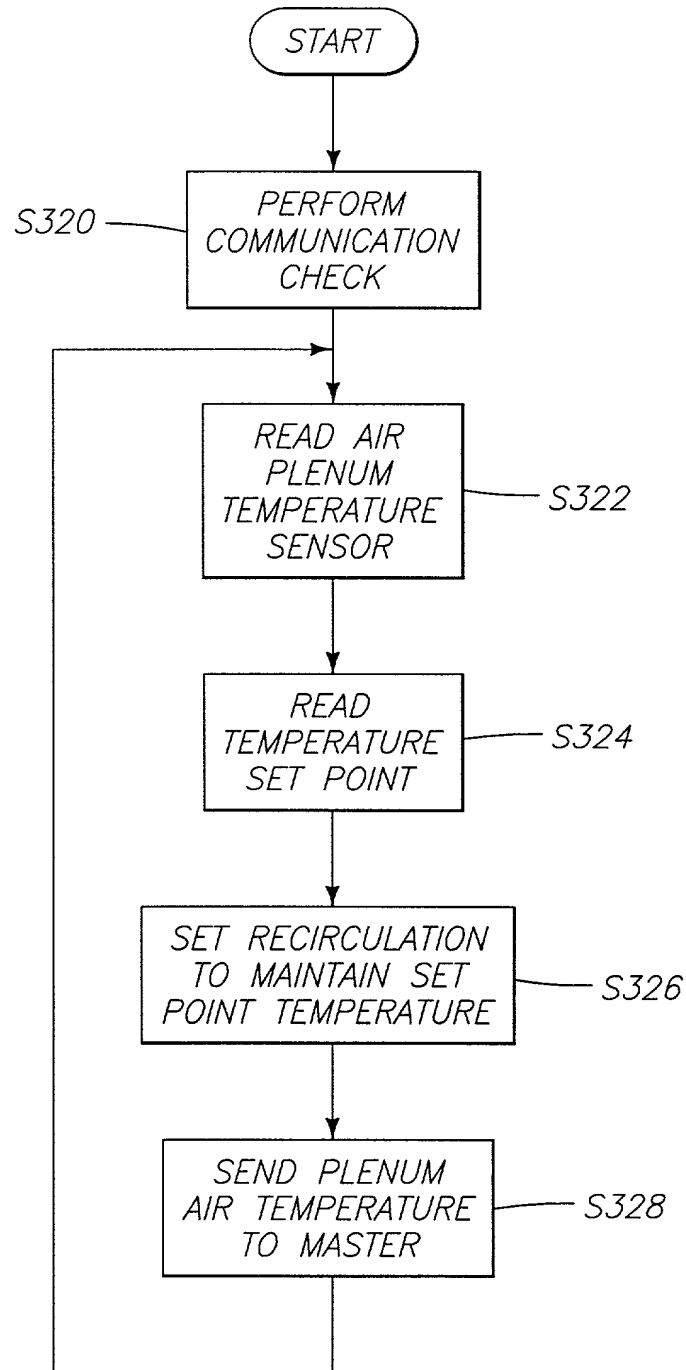




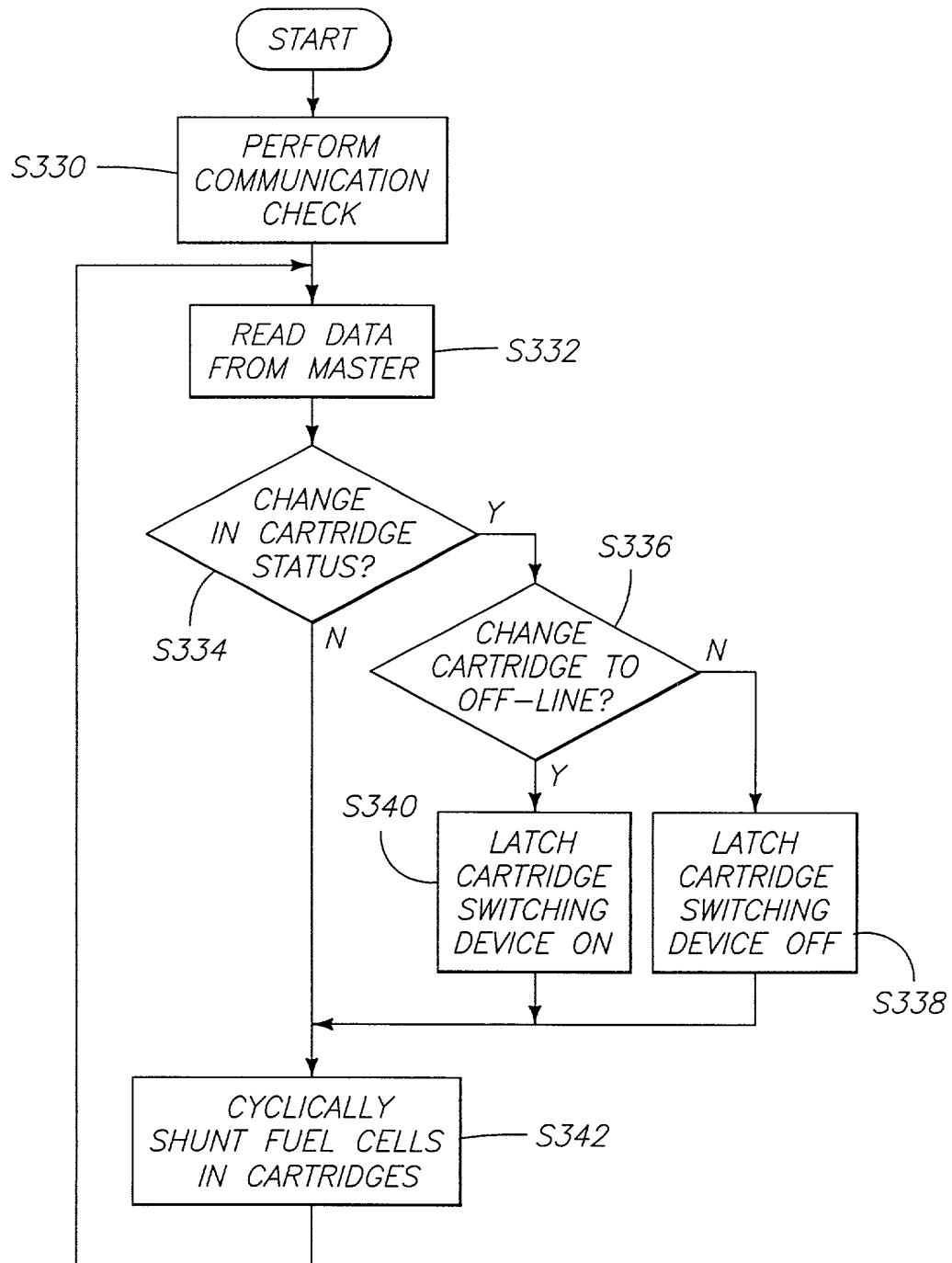
25/28



26/28

IF II 20 26

27/28

II II II II II

28/28

